

**PATENT NUMBER and  
ISSUE DATE**

**U.S. UTILITY Patent Application**

APPL NUM 10026245	FILING DATE 12/17/2001	CLASS 703	SUBCLASS 18	GAU 2123	EXAMINER <i>BRUDA, JAT Ferris</i>
<b>**APPLICANTS:</b> Becker Scott;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A CON OF 09/442,877 11/18/1999 WHICH IS A DIV OF 09/015,427 01/29/1993 PAT 6,016,390					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials				ARTCP012C	
TITLE : Memories having reduced bitline voltage offsets <i>in figure</i>					
U.S. DEPT. OF COMM. / PAT & TM-PTO-438L (Rev. 12-94)					

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
Assistant Examiner		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Draw.	Fig. Draw. Print Fig.
Primary Examiner		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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